## **EAST Search History**

## **EAST Search History (Prior Art)**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	100		US-PGPUB; USPAT	OR	ON	2010/05/20 17:57
L2	88	((processor processing CPU multiprocessor multi-processor microprocessor) near3 shar\$3) same ((memory near3 mapped) (memory-mapped)) near3 (registers address space)	US-PGPUB; USPAT	OR	ON	2010/05/20 17:57
L3	403	(different unique distinct) near3 frequenc\$3 with (DSP processor CPU (process \$2 adj2 unit) core) same controller	US-PGPUB; USPAT; USOCR	OR	ON	2010/05/20 17:58
L4	62	L3 and ("711" "712" "710" "360").clas.	US-PGPUB; USPAT; USOCR	OR	ON	2010/05/20 17:58
L5	403	(different unique distinct) near3 frequenc\$3 with (DSP processor CPU (process \$2 adj2 unit) core) same controller	US-PGPUB; USPAT; USOCR	OR	ON	2010/05/20 17:58
L6	132	((disk servo) near5 controller) and (shar \$3 with (memory- mapped (memory adj2 map\$4)))	US-PGPUB; USPAT	OR	ON	2010/05/20 17:58

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